Project May15-24 Advisor: Ayman Fayed							
Class D amplifier	5/1/2015						
	Alan Stewart	Troy Bowers	Xilu Wang	Dana Conrad	Jing Li	Kyle Maginot	
Weekly Hours	7		7	7			
Cum Hours	33.5	21	34.5	43	13.5	39	
Milestones	Due Date	Est Delivery				Status	
EQ Design Compl.	10/18/2014	10/23/2014				Finishing	
Power Apm Design	10/30/2014	10/30/2014				Finishing	
PCB Designed	11/15/2014					Not Started	
Bill of Materials	11/30/2014					Not Started	
Description		Target Date	Priority	Assigned To		Status	
	I						
Risks and Issues							
Next Actions							

	Individual Activity Report							
Alan Stewart	Team Lead							
	Design Document Drafting				4			
	Amplifier Design							
	Team Meetings							
				Total	7			
Troy Bowers	Communications							
	Created visuals for updated amplifier section of Design document.							
	Completed PSPICE designs for Amplifier portion. Ready to begin simulations							
				Total	7.5			
Xilu Wang	Web Master							
	Revising Website				1			
	Working on user interface				2			
	Team Meeting				1			
	Working on design doc				3			
				Total	7			
Dana Conrad					3.5			
	Realizing EQ component values (to values we can buy)							
	Writing Design Document							
	Team meetings							
				Total	7			
Jing Li	Key Concept Holder							
				Total	C			
Kyle Maginot								
				Total	C			

## EQ update

This week we went through the buffer and filter stages to realize the component values. Previously they were straight from the calculations, and many were too far from a component value we could actually buy. In some instances we were forced to combine two resistors or capacitors to create the value we needed.

## Next week's goals:

Next week we hope to start learning the PCB process and revisiting the EQ's power section (virtual ground circuit) to create a more robust virtual ground system, as we have decided to use a single 5v power supply to power the EQ side of things.. We also hope to focus more on the amplifier part now that this is fairly far along.