Project May15-24 Advisor: Ayman Fayed							
Class D amplifier	5/1/2015						
	Alan Stewart	Troy Bowers	Xilu Wang	Dana Conrad	Jing Li	Kyle Maginot	
Weekly Hours	7		7	5			
Cum Hours	33.5	21	34.5	48	13.5	45	
Milestones	Due Date	Est Delivery				Status	
EQ Design Compl.	10/18/2014	10/23/2014				Finishing	
Power Apm Design	10/30/2014	10/30/2014				Finishing	
PCB Designed	11/15/2014					Not Started	
Bill of Materials	11/30/2014					Not Started	
Description	,	Target Date	Priority	Assigned To		Status	
Risks and Issues							
Next Actions							

	Individual Activity Report							
Alan Stewart	Team Lead							
	Design Document Drafting				4			
	Amplifier Design							
	Team Meetings							
				Total	-			
Troy Bowers	Communications							
	Created visuals for updated amplifier section of Design document.							
	Completed PSPICE designs for Amplifier portion. Ready to begin simulations							
				Total	7.5			
Xilu Wang	Web Master							
	Revising Website				•			
	Working on user interface				2			
	Team Meeting				•			
	Working on design doc							
				Total				
Dana Conrad								
	Team Meetings							
	Learning PCB Tools							
	Beginning EQ PCB work							
				Total				
Jing Li	Key Concept Holder							
				Total				
Kulo Mosic -+				Total	(			
Kyle Maginot								
				Total	(			

## **EQ Status**

The EQ design is (hopefully) mostly complete by now. We reverted from planning on a 5v power supply to a 12v supply. The power section has been redesigned from a voltage divider to a TLE2426 which should provide a stable virtual ground and at 20 mA should be able to sink enough current for our (low power) needs. After several hours of messing around with the PCB workflow in cadence, it has been hopefully worked out to the point where we can start going through the process.

## **EQ TODO:**

We hope to start working through the PCB process soon to account for possible mistakes and bugs in the process.